Serial Number: 10/003,238

Filing Date: October 26, 2001

Title: ELECTRONIC ASSEMBLIES WITH FILLED NO-FLOW UNDERFILL (as amended)

Assignee: Intel Corporation

IN THE CLAIMS

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Please amend the claims as follows.

Claims 1-21. (Canceled)

22. (Previously Presented) A component package fabricated by:

depositing an underfill material over a plurality of pads in a component-mounting area of a substrate, the underfill material comprising a filler material containing particles;

placing a component on the component-mounting area, such that terminals of the component are aligned with corresponding pads and substantially enveloped in the underfill material, the particles potentially inhibiting a suitable connection between corresponding terminals and pads unless the particles are substantially removed; and

applying suitable pressure to cause the terminals to physically contact the pads and to remove substantially all potentially inhibiting particles from between corresponding terminals and pads.

- 23. (Previously Presented) The component package recited in claim 35 and fabricated such that the operations of applying suitable pressure and suitable heat are performed substantially concurrently by apparatus from the group comprising a thermocompression bonder, an ultrasonic bonder, and a component placement tool.
- 24. (Previously Presented) The component package recited in claim 35 and fabricated such that the pads are pre-coated with solder, and wherein, in applying suitable heat, the terminals become attached to the pads through the solder.
- 25. (Previously Presented) The component package recited in claim 35 and fabricated such that the terminals are pre-coated with solder, and wherein, in applying suitable heat, the terminals become attached to the pads through the solder.
- 26. (Previously Presented) The component package recited in claim 22, wherein the filler material is selected from the group consisting of silica, silicon oxide, silicon dioxide, silicon nitride, aluminum oxide, and aluminum nitride.

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27. (Currently Amended) An electronic assembly comprising at least one integrated circuit (IC) package fabricated by:

depositing an underfill material over a plurality of pads in an IC mounting area of a substrate, the underfill material comprising a filler material containing particles;

placing an IC on the IC mounting area, such that terminals of the IC are aligned with corresponding pads and substantially enveloped in the underfill material, the particles potentially inhibiting a suitable connection between corresponding terminals and pads unless the particles are substantially removed; and

applying suitable pressure to cause the terminals to physically contact the pads and to squeeze out remove substantially all potentially inhibiting particles from between corresponding terminals and pads.

- 28. (Previously Presented) The electronic assembly recited in claim 36 and fabricated such that the operations of applying suitable pressure and suitable heat are performed substantially concurrently by apparatus from the group comprising a thermocompression bonder, an ultrasonic bonder, and a component placement tool.
- 29. (Previously Presented) The electronic assembly recited in claim 36, wherein the underfill material comprises a filler material selected from the group consisting of silica, silicon oxide, silicon dioxide, silicon nitride, aluminum oxide, and aluminum nitride.

Claims 30-34. (Canceled)

- 35. (Previously Presented) The component package recited in claim 22, further fabricated by applying suitable heat to melt solder situated between the terminals and pads, which when cooled results in an electrical and mechanical connection between corresponding terminals and pads.
- 36. (Previously Presented) The electronic assembly recited in claim 27, further fabricated by applying suitable heat to melt solder situated between the terminals and pads, which when cooled results in an electrical and mechanical connection between corresponding terminals and pads.

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37. (Currently Amended) An electronic assembly comprising: at least one integrated circuit [[(IC)]] package having a plurality of terminals; a substrate having a plurality of pads coupled to the plurality of terminals via a corresponding plurality of connections;

a hardened underfill encapsulating the terminals, pads, and connections; and a plurality of particles in the underfill, except that substantially no particles are in the eonnections, wherein some of the particles are of such size and shape eomposition as to potentially inhibit suitable physical and electrical contact eonnections between corresponding terminals and pads if such particles remained [[are]] in the connections, and wherein any particles remaining in the connections are of such size and shape that they do not prevent adequate physical and electrical contact between corresponding terminals and pads.

- 38. (Previously Presented) The electronic assembly recited in claim 37, wherein the particles have a size in the range of 0.05 microns to 40 microns.
- 39. (Previously Presented) The electronic assembly recited in claim 37, wherein the particles are substantially spherical.
- 40. (Previously Presented) The electronic assembly recited in claim 37, wherein the particles comprise material selected from the group consisting of silica, silicon oxide, silicon dioxide, silicon nitride, aluminum oxide, aluminum nitride, ceramic oxide, and ceramic nitride.

Claims 41-45. (Canceled)

- 46. (New) The component package recited in claim 35, wherein the underfill material further comprises a fluxing agent, and wherein, in the operation of applying suitable heat, the fluxing agent cleans the terminals and the pads.
- 47. (New) The component package recited in claim 36, wherein the underfill material further comprises a fluxing agent, and wherein, in the operation of applying suitable heat, the fluxing agent cleans the terminals and the pads.

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48. (New) An electronic assembly comprising:

at least one integrated circuit package having a plurality of terminals;

a substrate having a plurality of pads to be coupled to the plurality of terminals via a corresponding plurality of connections; and

an unhardened underfill covering the terminals and pads;

wherein the underfill, when heated, is to harden;

wherein the underfill, when hardened, is to have a coefficient of thermal expansion;

wherein the underfill comprises a plurality of particles to reduce the coefficient of thermal expansion;

wherein the particles are of such size and shape as to potentially inhibit suitable connections between corresponding terminals and pads;

wherein the terminals are to physically contact the pads, and wherein substantially all of the particles are to be removed from between the terminals and the pads, when suitable pressure is applied to the package and/or the substrate; and

wherein the underfill further comprises a fluxing agent to clean the terminals and pads when suitable heat is applied to the electronic assembly to connect the terminals and pads.

- 49. (New) The electronic assembly recited in claim 48, wherein the particles have a size in the range of 0.05 microns to 40 microns.
- 50. (New) The electronic assembly recited in claim 48, wherein the particles are substantially spherical.
- 51. (New) The electronic assembly recited in claim 48, wherein the particles are selected from the group consisting of silica, silicon oxide, silicon dioxide, silicon nitride, aluminum oxide, aluminum nitride, ceramic oxide, and ceramic nitride.
- 52. (New) The electronic assembly recited in claim 48, wherein the fluxing agent is selected from the group consisting of an organic carboxylic acid, a polymeric fluxing agent that has one or more carboxylic acid groups, an organic compound that contains one or more hydroxyl groups, or a mixture thereof.

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